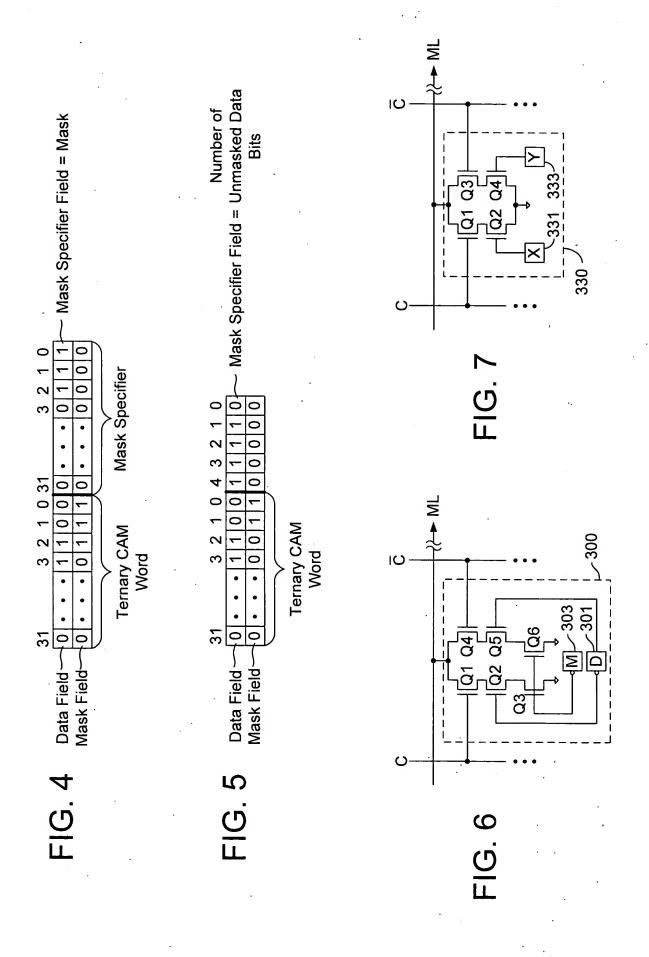


FIG. 3



√MSEL (410 RW 401 MSL 407 DBUS / MASK 405 DATA 403 408 /